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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 10/707287

Conf. No.: 1286

Examiner: Marcus Kirk

Art Unit: 2825

Filing Date: 12/3/2003

Docket No.: BUR920030123US1  
(TBMB-0036)

Applicant: Gray et al.

Title: CIRCUIT AREA MINIMIZATION USING SCALING

## COMMISSIONER FOR PATENTS

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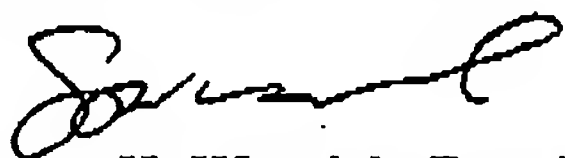
## STATUS INQUIRY

Sir:

Applicant's representative respectfully requests the status of the above-referenced patent application.

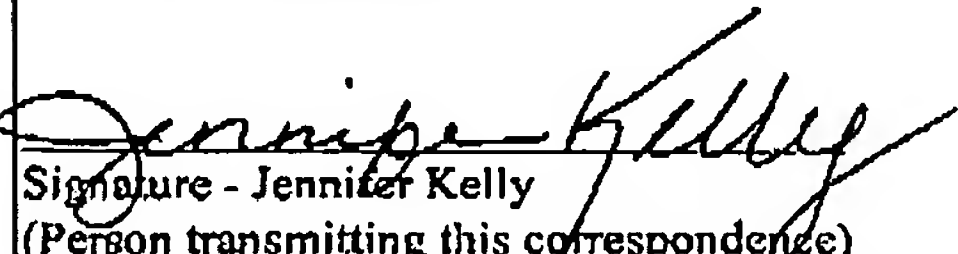
Respectfully submitted,

Date: February 13, 2006

  
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